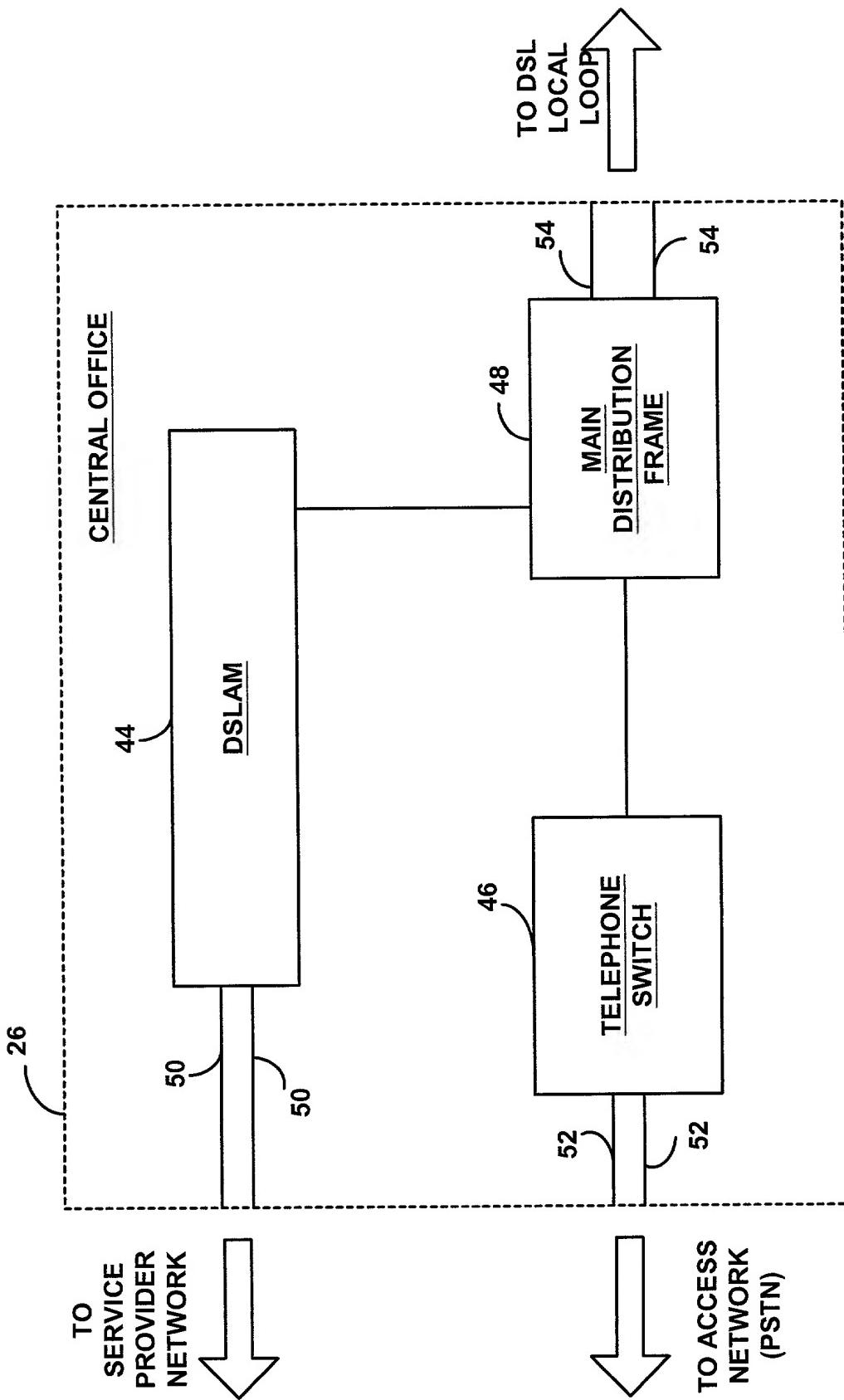
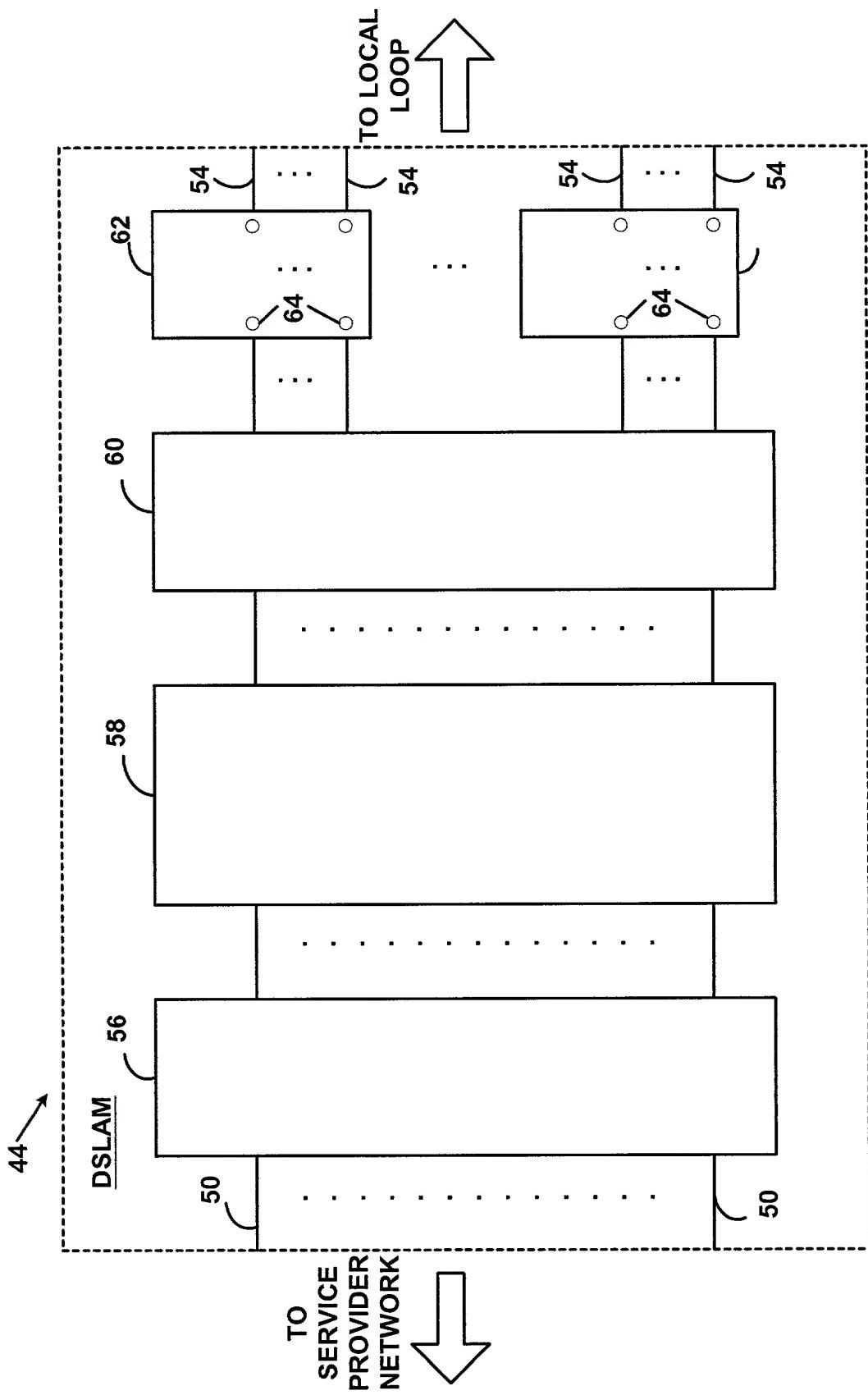


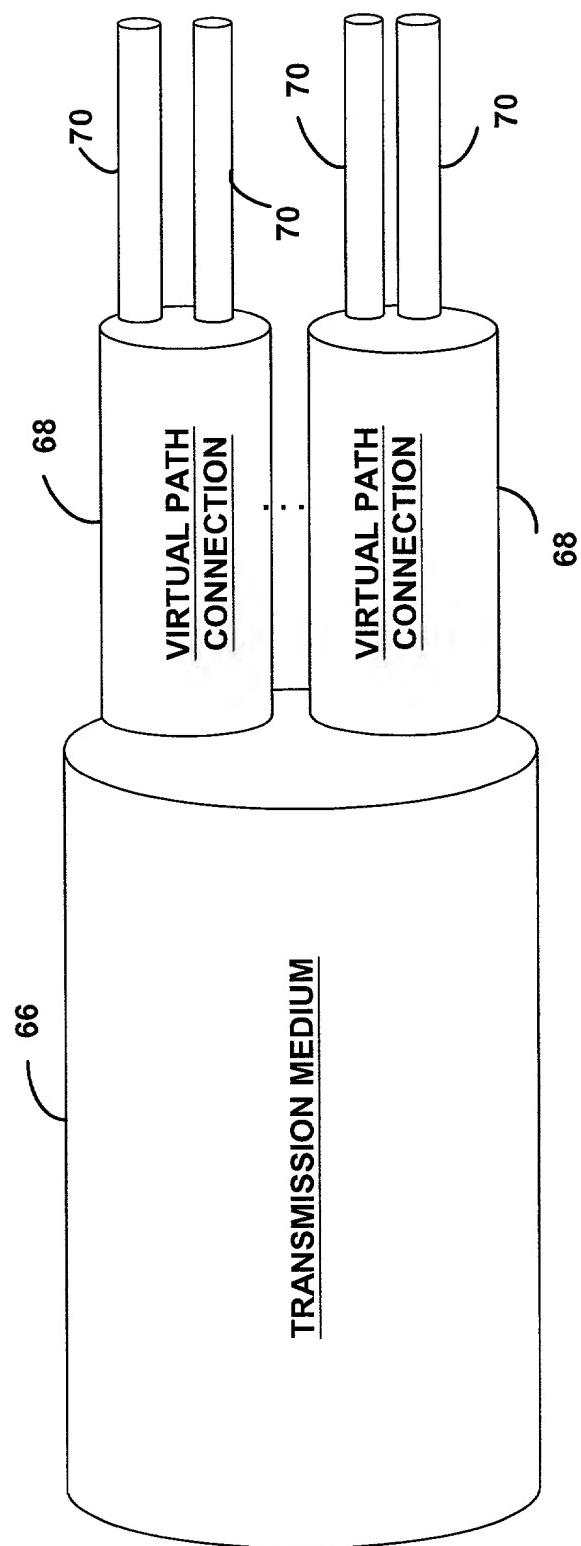
**FIG. 2**

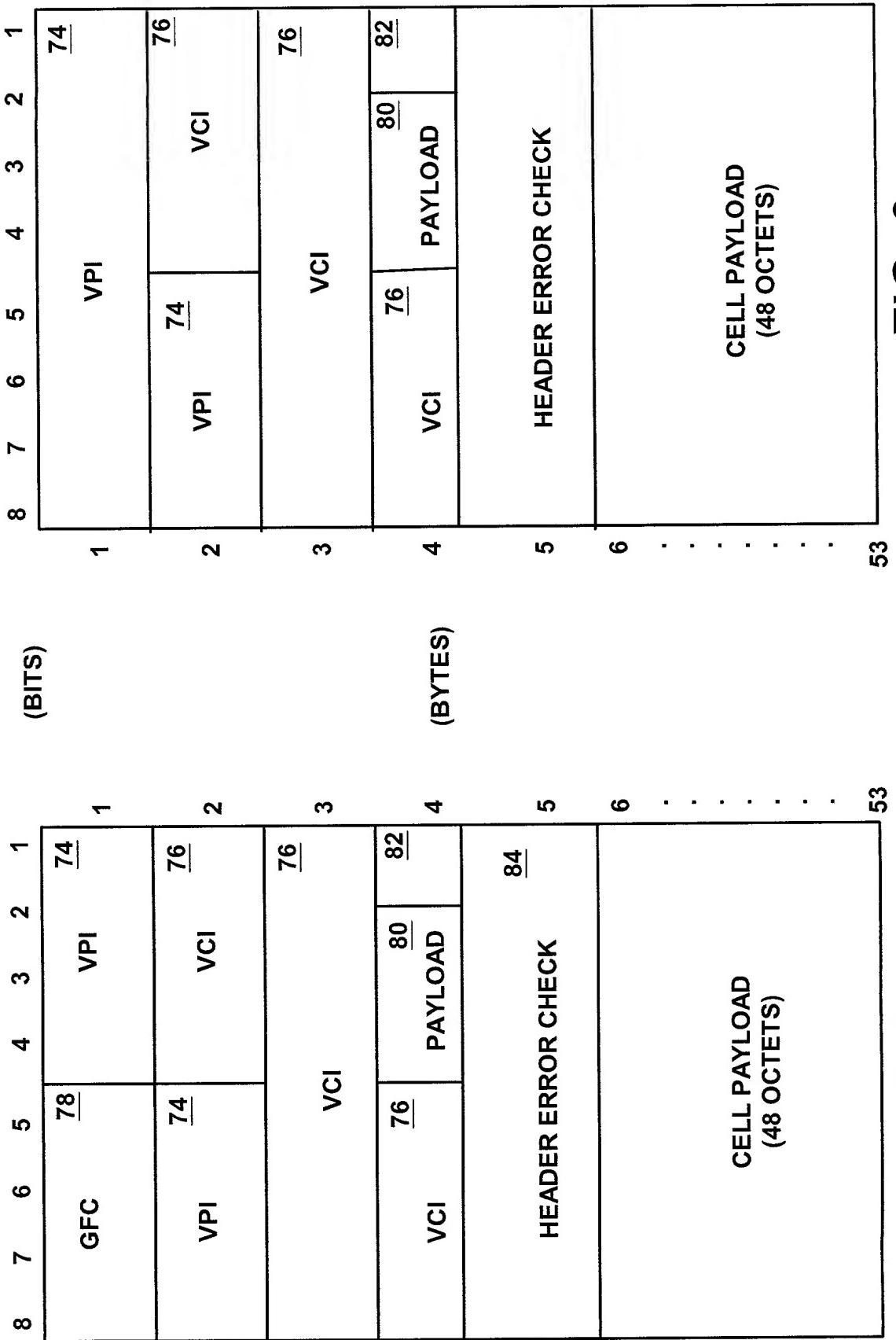




**FIG. 3**

**FIG. 4**





5

FIG. 6

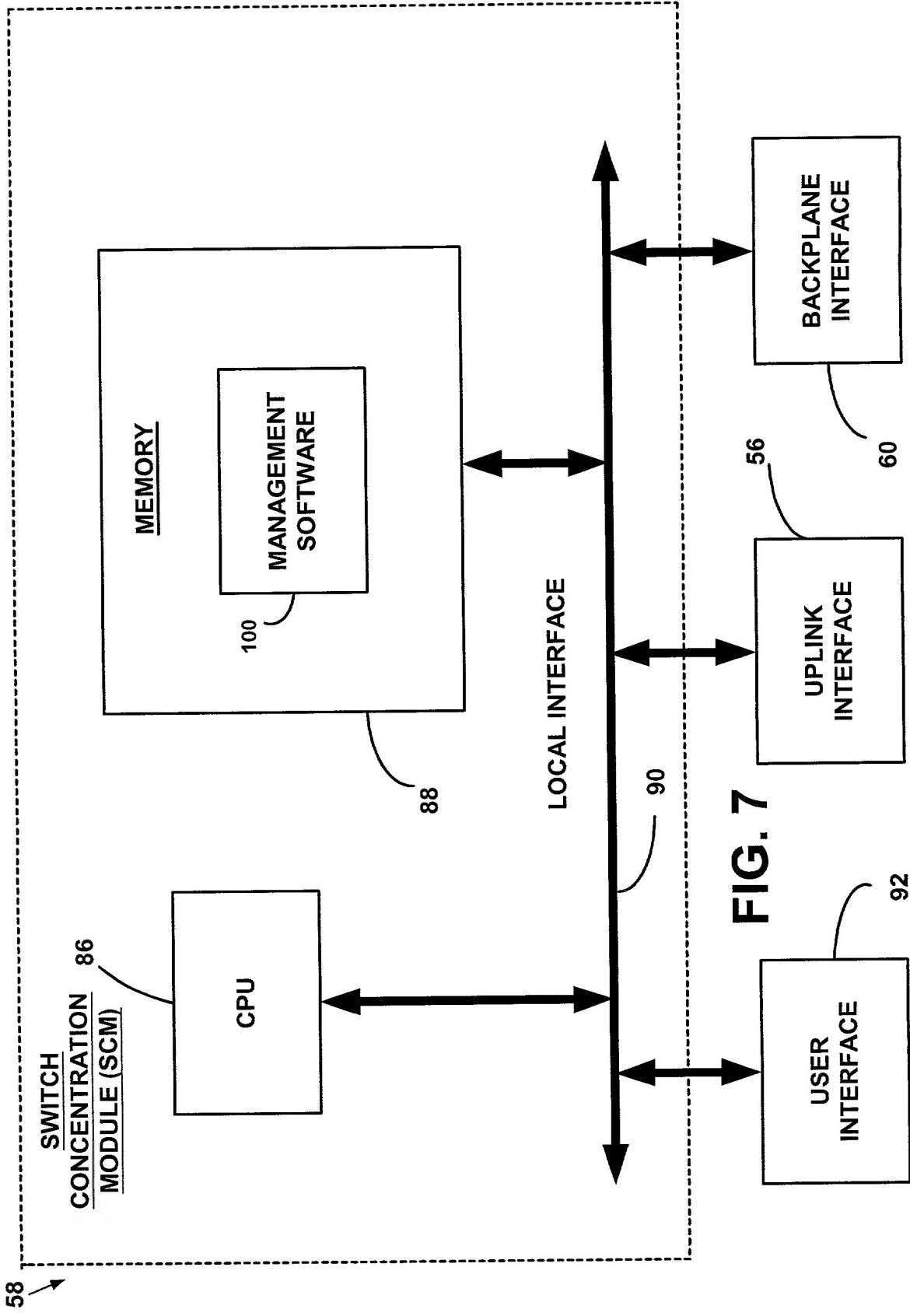
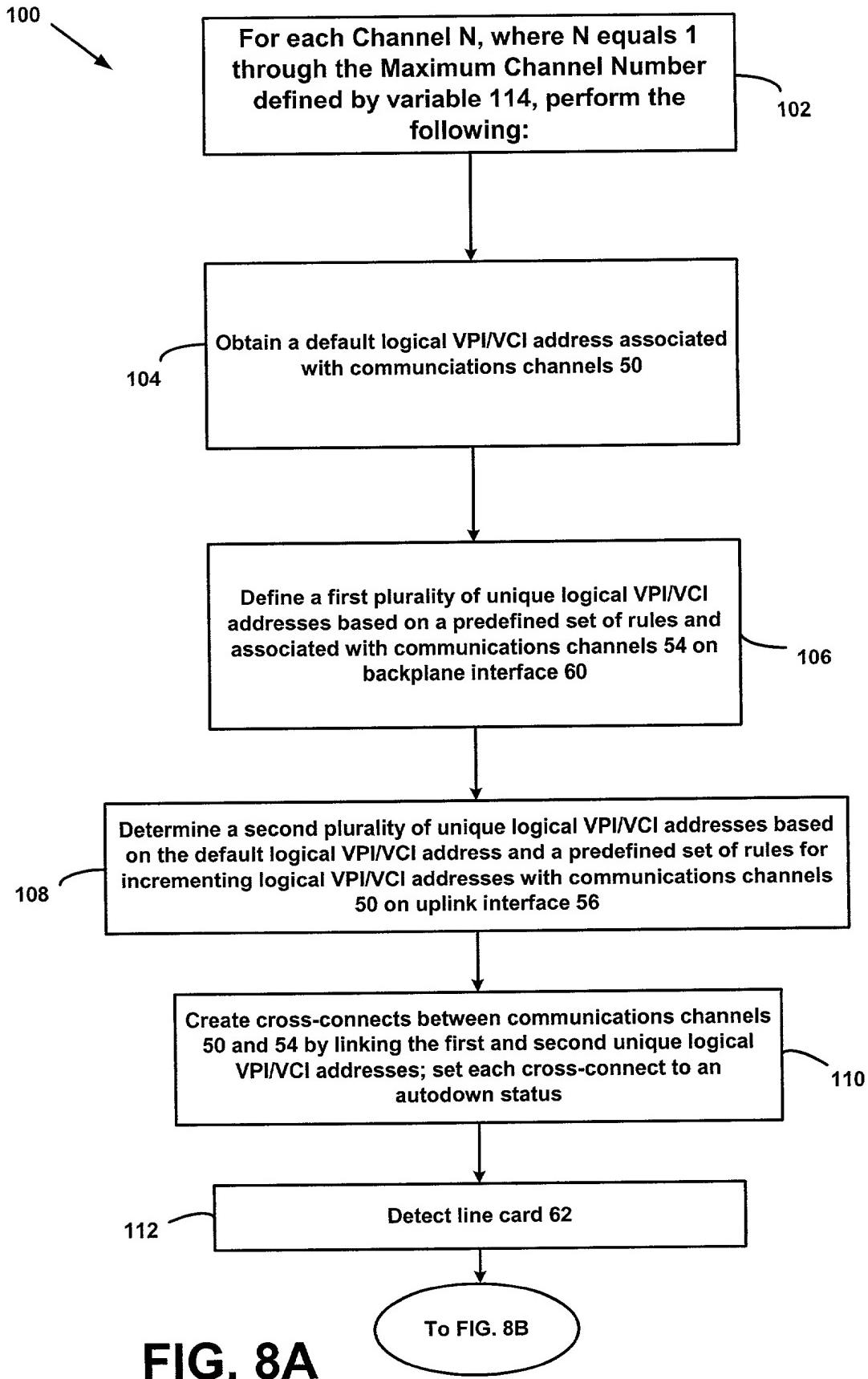
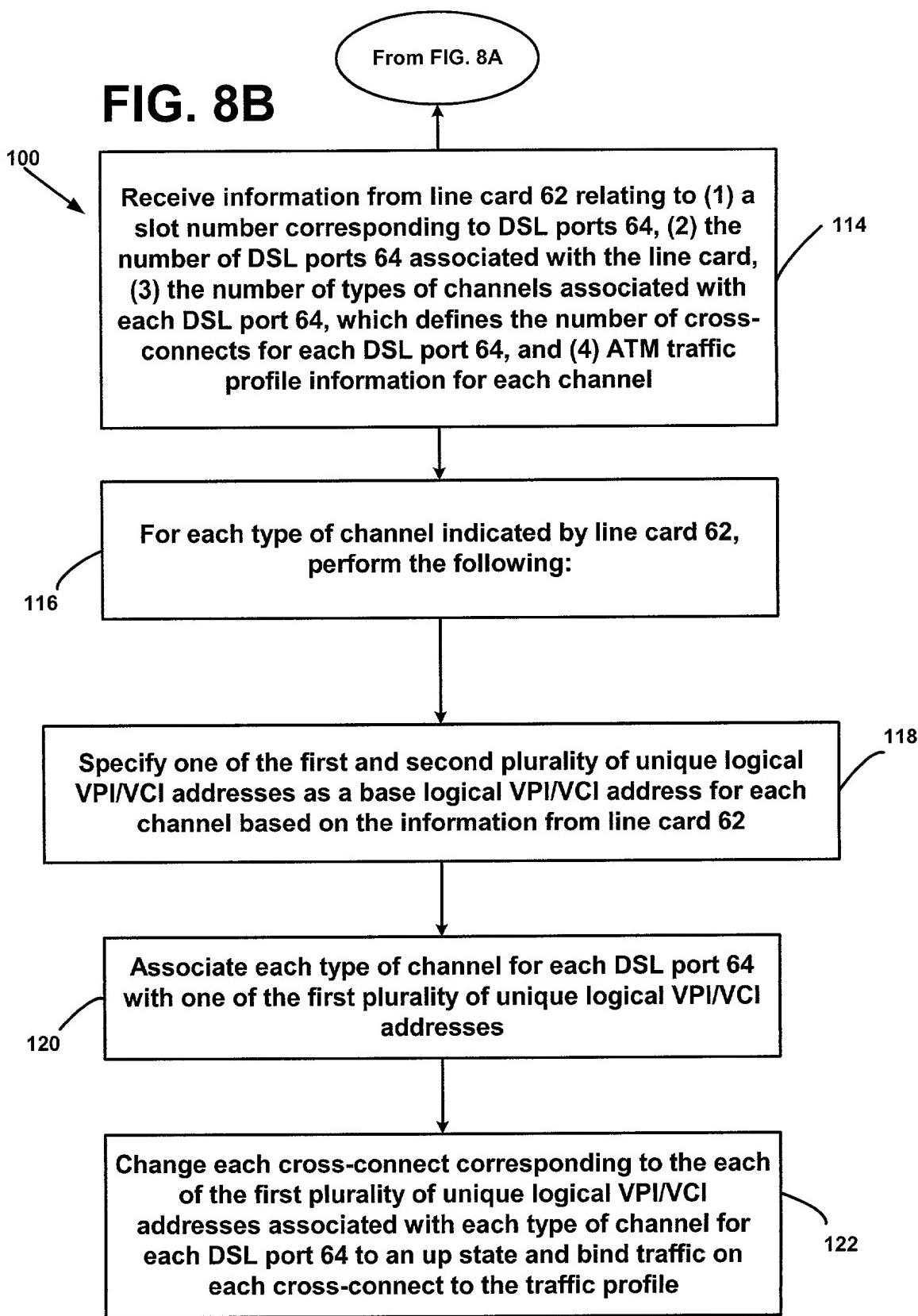


FIG. 7



**FIG. 8A**

## FIG. 8B



<u>144</u>	LINE CARD VARIABLE	VALUE
<u>146</u>	SLOT #	
<u>148</u>	NUMBER OF PORTS	
<u>150</u>	REQUESTED NUMBER OF CHANNELS PER PORT	
<u>152</u>	REQUESTED TRAFFIC PROFILE INDICATOR PER CHANNEL	

**FIG. 9**

<u>154</u>	DSL PORT VARIABLE	VALUE
<u>154</u>	DSL PORT #	
<u>156</u>	MAX VPI	
<u>158</u>	MAX VCI	
<u>160</u>	STATUS	
<u>162</u>	CONFIGURATION PARAMETERS (# channels, ATM parameters, upstream and downstream rate table, etc.)	

**FIG. 10**

<u>166</u>	<b>BACKPLANE INTERFACE</b>	<b>VALUE</b>
<b>VARIABLE</b>		
<u>168</u>	<b>INTERFACE ID</b>	
<u>170</u>	<b>MAX VPI</b>	
<u>172</u>	<b>MAX VCI</b>	
<u>174</u>	<b>STATUS</b>	
<u>176</u>	<b>OTHER PARAMETERS</b>	

**FIG. 11**

<u>178</u>	<b>UPLINK INTERFACE</b>	<b>VALUE</b>
<b>VARIABLE</b>		
<u>180</u>	<b>INTERFACE ID</b>	
<u>182</u>	<b>MAX VPI</b>	
<u>184</u>	<b>MAX VCI</b>	
<u>186</u>	<b>STATUS</b>	
<u>188</u>	<b>OTHER PARAMETERS</b>	

**FIG. 12**

<b>190</b>	<b>CROSS-CONNECT VARIABLE</b>	<b>VALUE</b>
<u>192</u>	<b>CROSS CONNECT ID</b>	
<u>194</u>	<b>IFINDEX1</b>	
<u>196</u>	<b>VPI1</b>	
<u>200</u>	<b>VCI1</b>	
<u>202</u>	<b>IFINDEX2</b>	
<u>204</u>	<b>VPI2</b>	
<u>206</u>	<b>VCI2</b>	

**FIG. 13**

**FIG. 14A**

CROSS-CONNECTION TABLE	
210	212      UPLINK INTERFACE:VPI:VCI
216	214      BACKPLANE INTERFACE:VPI:VCI
	<p><b>[UPLINK INTERFACE = Ifup = 1]</b></p> <p><b>[VPI0 ≤ VPI ≤ VPlm]</b></p> <p><b>[VC10 ≤ VC ≤ VCIm]</b></p> <p><b>[p = number of ports per card]</b></p> <p><b>[c = number of cards in system]</b></p>
	<p>Ifup:VPI0:VC10</p> <p>Ifup:VPI0:VC10+1</p> <p>Ifup:VPI0:VC10+p-2</p> <p>Ifup:VPI0:VC10+p-1</p> <p>Ifup:VPI0:VC10+p</p> <p>Ifup:VPI0:VC10+p+1</p> <p>Ifup:VPI0:VC10+p*2-2</p> <p>Ifup:VPI0:VC10+p*2-1</p> <p>Ifup:VPI0:VC10+p*(c-2)</p> <p>Ifup:VPI0:VC10+p*(c-2)+1</p>
	<p>If1:VPI0:VC10</p> <p>If1:VPI0+1:VC10</p> <p>If1:VPI0+p-2:VC10</p> <p>If1:VPI0+p-1:VC10</p> <p>If2:VPI0/ VC10</p> <p>If2:VPI0+1:VC10</p> <p>If2:VPI0+p-2:VC10</p> <p>If2:VPI0+p-1:VC10</p> <p>If2:VPI0/ VC10</p> <p>If2:VPI0+1:VC10</p>

**FIG. 14B**

CROSS-CONNECTION TABLE	
<u>210</u> <b>UPLINK INTERFACE:VPI:VCI</b>  <b>[UPLINK INTERFACE = Ifup = 1]</b> <b>[VPI0 ≤ VPI ≤ VPIm]</b> <b>[VC10 ≤ VCI ≤ VCIm]</b> <b>[p = number of ports per card]</b> <b>[c = number of cards in system]</b>	<u>216</u> <b>STATUS</b>  <b>[IF1 ≤ BACKPLANE INTERFACE:VPI:VCI]</b>  <b>[IF1 ≤ BACKPLANE INTERFACE:VPI:VCI]</b> <b>[VPI0 = fixed starting VPI]</b> <b>[VC10 fixed starting VCI]</b> <b>[p = number of ports per card]</b> <b>[c = number of cards in system]</b>
<u>212</u> <b>UPLINK INTERFACE:VPI:VCI</b>  <b>[UPLINK INTERFACE = Ifup = 1]</b> <b>[VPI0 ≤ VPI ≤ VPIm]</b> <b>[VC10 ≤ VCI ≤ VCIm]</b> <b>[p = number of ports per card]</b> <b>[c = number of cards in system]</b>	<u>214</u> <b>BACKPLANE INTERFACE:VPI:VCI</b>  <b>[IF1 ≤ BACKPLANE INTERFACE:VPI:VCI]</b>  <b>[IF1 ≤ BACKPLANE INTERFACE:VPI:VCI]</b> <b>[VPI0 = fixed starting VPI]</b> <b>[VC10 fixed starting VCI]</b> <b>[p = number of ports per card]</b> <b>[c = number of cards in system]</b>

**FIG. 14C**

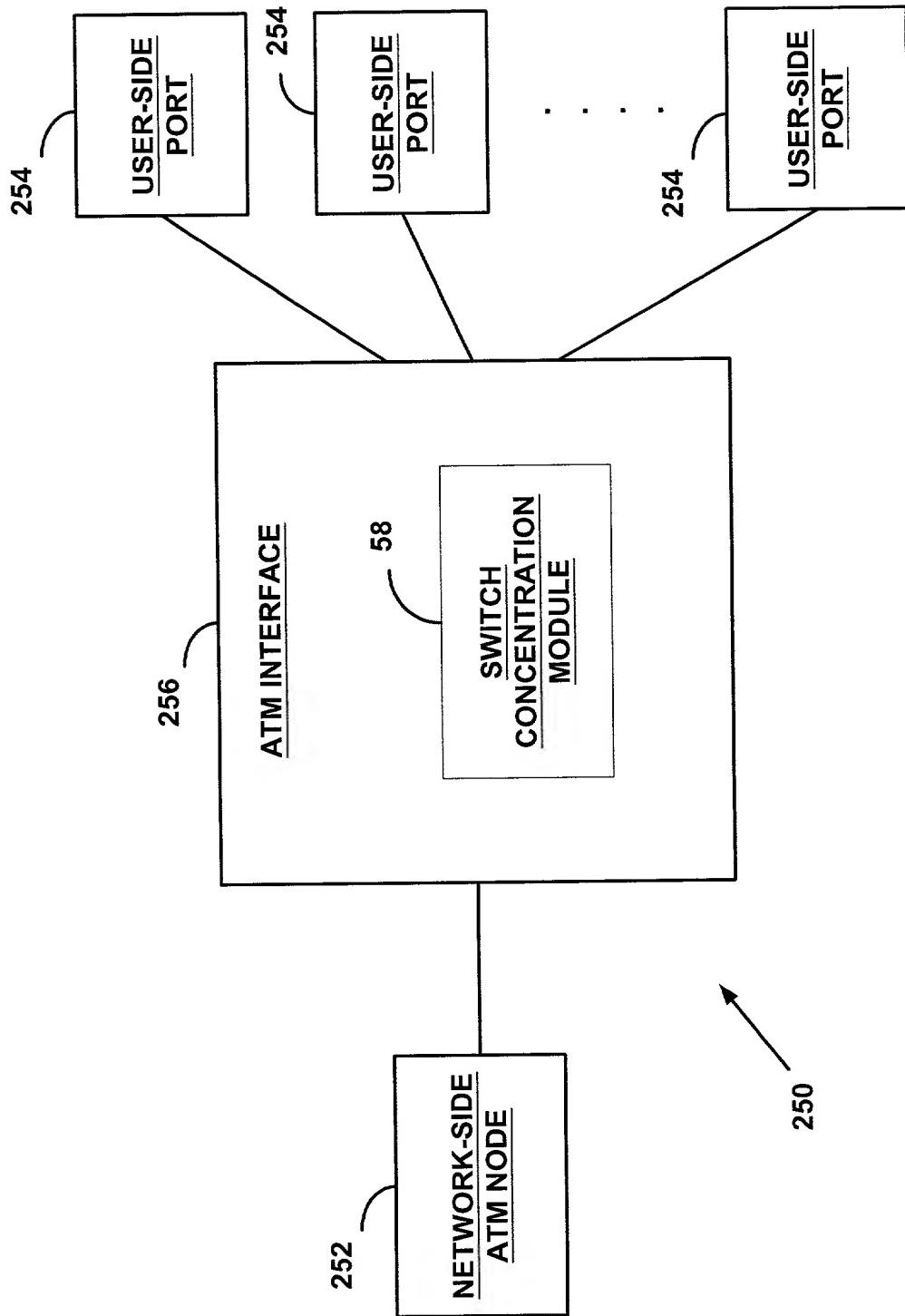
<b>CROSS-CONNECTION TABLE</b>	
<b>210</b>	<b>212    UPLINK INTERFACE VPI:VCI</b>  <b>[UPLINK INTERFACE = Ifup = 1]</b> <b>[VPI0 ≤ VPI ≤ VPIm]</b> <b>[VCIO ≤ VCI ≤ VCIm]</b> <b>[p = number of ports per card]</b> <b>[c = number of cards in system]</b>
<b>216</b>	<b>214    STATUS</b>  <b>[IF1 ≤ BACKPLANE INTERFACE ≤ IFc]</b> <b>[VPI0 = fixed starting VPI]</b> <b>[VCIO fixed starting VCI]</b> <b>[p = number of ports per card]</b> <b>[c = number of cards in system]</b>

<u>220</u>	VCL VARIABLE	VALUE
<u>222</u>	IFINDEX	
<u>224</u>	VPI	
<u>226</u>	VCI	
<u>228</u>	TRAFFIC PROFILE UP	
<u>230</u>	TRAFFIC PROFILE DOWN	

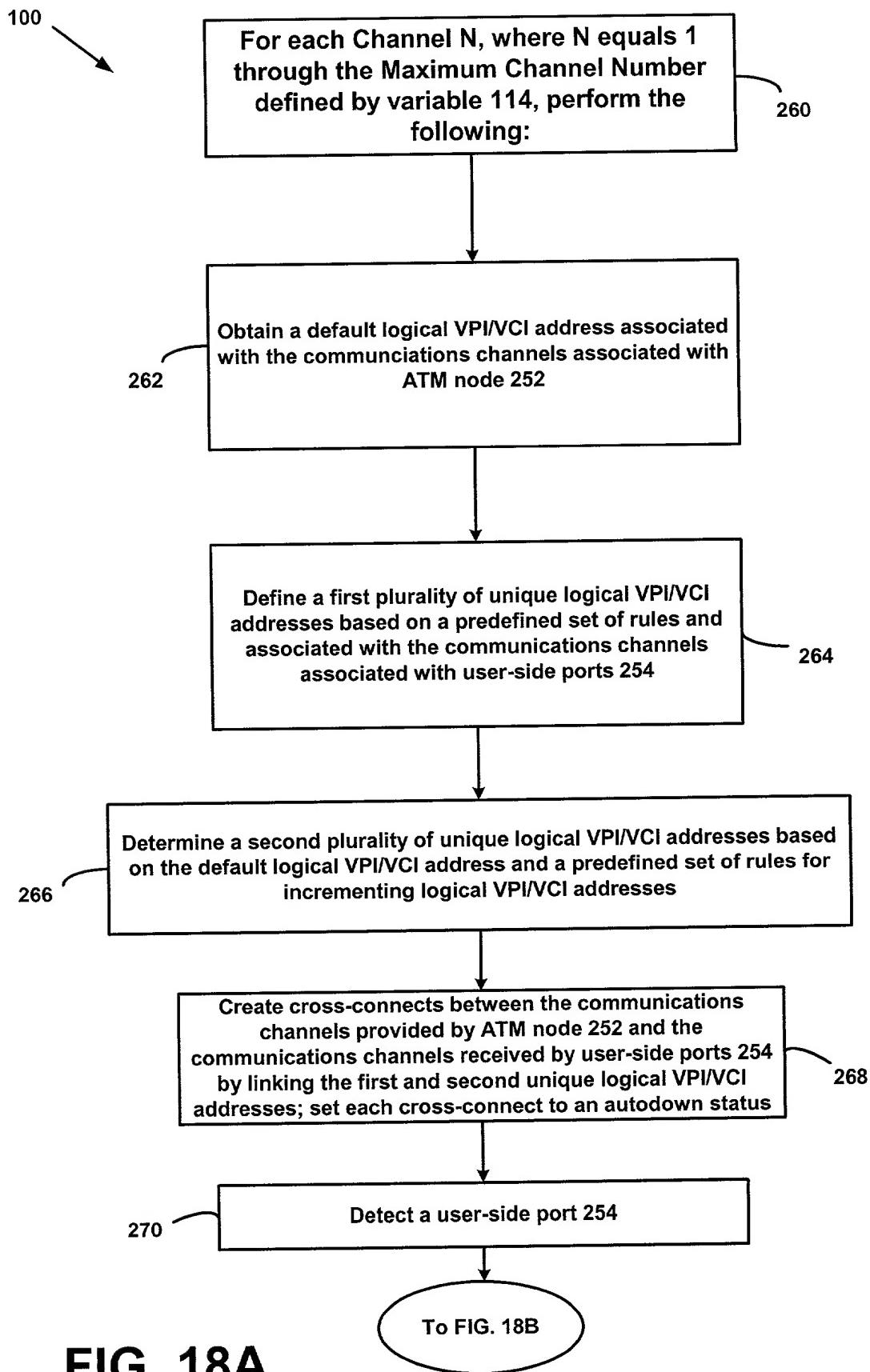
**FIG. 15**

<u>232</u>		AUTO-CONFIGURATION RECORD
	AUTO-CONFIGURATION VARIABLE	VALUE
<u>234</u>	INTERFACE ID	
<u>236</u>	CHANNEL	
<u>238</u>	BASE VPI	
<u>240</u>	BASE VCI	

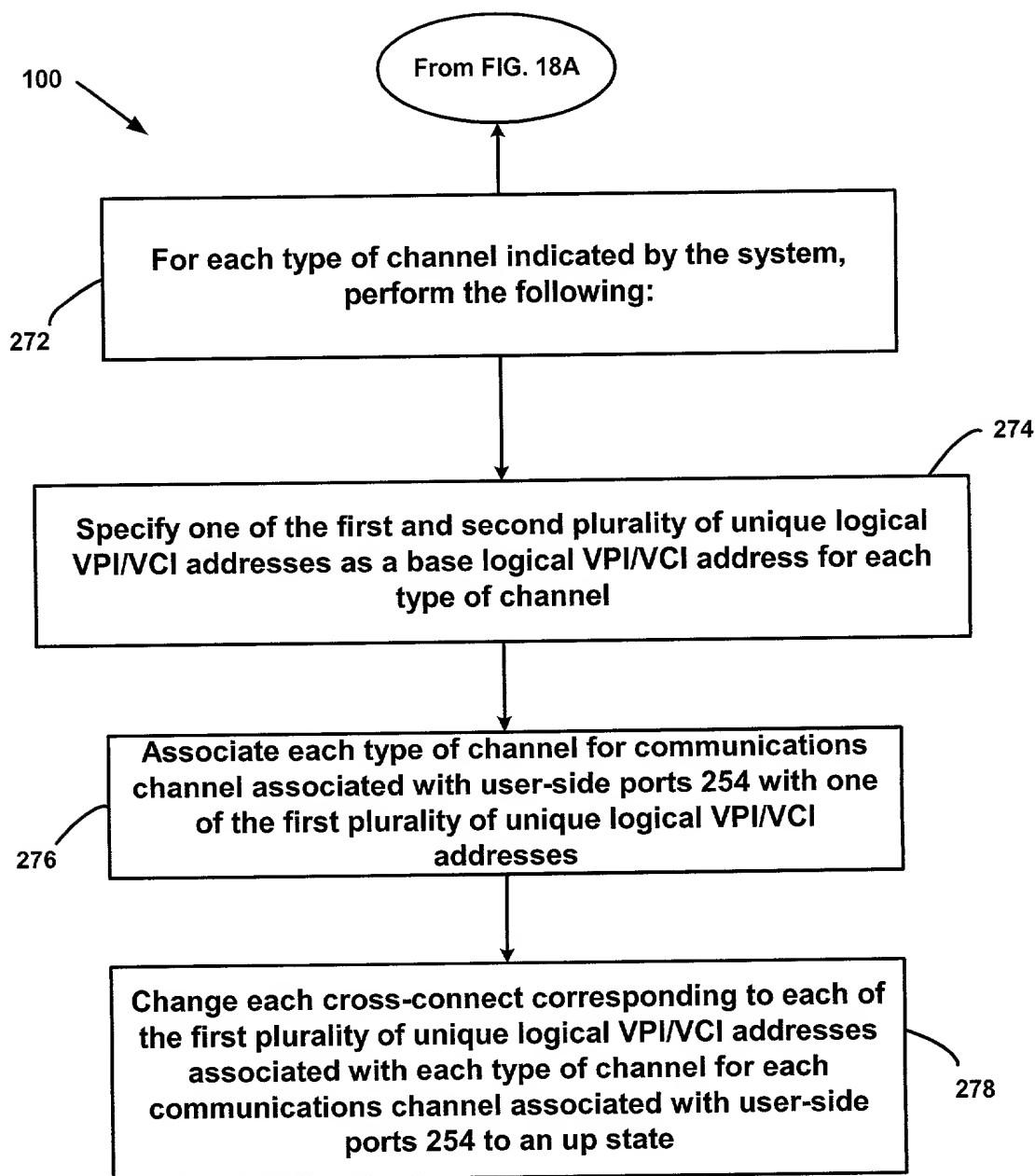
**FIG. 16**



**FIG. 17**



**FIG. 18A**



**FIG. 18B**